

## **METHODS OF FORMING BORON CARBO-NITRIDE LAYERS FOR INTEGRATED CIRCUIT DEVICES**

### **CROSS-REFERENCE TO RELATED APPLICATIONS**

The present application claims priority from United States Provisional Patent Application No. 60/463,570, filed April 17, 2003, the disclosure of which is incorporated herein by reference in its entirety.

### **FIELD OF THE INVENTION**

The present invention relates to compositions and methods for depositing a metal layer.

### **BACKGROUND OF THE INVENTION**

The microelectronics industry is continuously scaling down feature sizes and developing new material applications in the pursuit of smaller and faster devices. One focus towards this goal is the implementation of copper into multi-level interconnect schemes for microprocessor and memory devices as copper offers improved electromigration resistance and higher electrical conductivity versus aluminum-based interconnects. The semiconductor industry is introducing copper interconnects as a replacement for conventional aluminum and aluminum alloy interconnects for future generations of semiconductor devices. With its greater current carrying capacity, the introduction of copper interconnects should reduce device geometry, power consumption and heat generation. However, copper is a fast diffuser in silicon and diffuses in dielectrics, resulting in a deterioration of devices at low temperatures. To avoid unwanted migration of copper atoms, the copper wiring should be encapsulated with a barrier layer, *e.g.*, barrier diffusion layer, to prevent diffusion of copper. The encapsulation of copper wiring is commonly done using a combination of an underlayer of a low-resistivity transition metal-based material and an insulating layer comprised of a Si-based film. Each of these barriers may be comprised of more than one material to maximize properties and performance, *i.e.* a bilayer of Ta/TaN or bilayer of SiN<sub>x</sub>/SiC<sub>x</sub>N<sub>y</sub>. In order to minimize the capacitance contribution of the insulating diffusion barrier to the RC time delay, a desirable insulating barrier may have a low dielectric constant, *k*, to limit overall capacitance when integrated into the interconnect design.

One method of providing the insulating barrier layer is through plasma enhanced chemical vapor deposition (PECVD). Thermal chemical vapor deposition (TCVD) is a process in which a flow of gaseous reactants over a heated semiconductor substrate chemically react to deposit a solid layer on the heated substrate. PECVD is a process which introduces a plasma to activate the gaseous reactants. In each case, the flow of the reactants can be in parallel or in series, whereby a series flow of reactants is sometimes referred to as atomic layer deposition.

One current manufacturing technology for producing an insulating barrier layer generally is transitioning from plasma enhanced chemical vapor deposited (PECVD)  $\text{SiN}_x$  to PECVD  $\text{SiC}_x\text{N}_y$  and  $\text{SiC}_x$  because these materials generally have a lower dielectric constant. Recent studies have reported PECVD  $\text{SiC}_x\text{N}_y$  films with dielectric constants of approximately 5 and PECVD  $\text{SiC}_x$  films with a dielectric constant less than 5, resulting in an approximately 25% lower dielectric constant compared to PECVD  $\text{SiN}_x$ . *See*, Martin et al., 2002 IEEE International Interconnect Technology Conference, Burlingame, CA, June 3-5, 2002, 42; and Fayolle et al., 2002 IEEE International Interconnect Technology Conference, Burlingame, CA, June 3-5, 2002, 39. However, PECVD film deposition can cause damage to the bulk insulating material, commonly referred to as the intermetal/intrametal dielectric. Also, current Si-based PECVD films may not meet future performance requirements, such as adhesion to other interconnect films, electromigration performance with copper, barrier properties, or low current leakage.

Additionally, boron has been utilized in high density plasma chemical vapor deposition. *See*, U.S. Patent No. 6,500,771. U.S. Patent No. 5,895,938 illustrates semiconductor devices using semiconductor boron carbo-nitride compounds such as a light-emitting device and a solar cell. The '938 patent illustrates a CVD process to form a boron carbo-nitride compound to grow into a crystalline structure wherein a substrate temperature at 850°C or more was used in the step of forming the boron carbo-nitride compound. U.S. Patent No. 6,424,044 shows a method of forming a boron carbide layer formed in a PECVD for use as a barrier and an etch-stop layer in a copper dual damascene structure. U.S. Patent No. 6,352,921 illustrates amorphous boron carbide is formed in a PECVD for use as a barrier and an etch-stop layer in a copper dual damascene structure. U.S. Patent No. 6,288,448 a process of combining

silane and ammonia in a boron rich atmosphere for semiconductor interconnect barrier material for use with copper interconnects.

$BC_x$  and  $BC_xN_y$  films have been reported with a dielectric constant ranging from 3 to 7. See, Sugino et al., *Applied Physics Letters*, Vol. 80, No. 4, 649; Gelatos et al., *MRS*, Vol. 260, 1992, 347; Levy et al., *MRS*, vol. 427, 1996, 469; and Nguyen et al., *J. Electrochem. Soc.*, Vol. 141, No. 6, 1994, 1633. For the deposition of boron carbo-nitride films using dimethylamine borane (DMAB), the increase in the dielectric constant with increasing temperature may be caused by a higher atomic density in the films that leads to a higher polarizability. Sugino, *et al.*, observed an increase in the dielectric constant with temperature for films deposited by PECVD using  $BCl_3/N_2/CH_4$ , while Gelatos, *et al.*, observed a higher temperature with a decreasing dielectric constant for PECVD films using  $B_2H_6/NH_3$ .

#### SUMMARY OF THE INVENTION

Embodiments of the present invention include methods for forming a boron carbo-nitride layer forming a boron carbo-nitride layer on a substrate by chemical vapor deposition using a boron source precursor comprising  $NR_3:BX_3$ , wherein X is selected from the group consisting of hydrogen and halide and wherein R is selected from the group consisting of hydrogen, alkyl, allyl, alkenyl, alkynyl alkylaryl, arylalkyl, phenyl, alkene and alkyne. The chemical vapor deposition can be a thermal chemical vapor deposition.

Other embodiments of the present invention can include a thermal chemical vapor deposition methods comprising providing a semiconductor substrate within a chemical vapor deposition chamber, vaporizing a boron source precursor with a vaporizer to form a flowing vaporized precursor stream, and directing the flowing vaporized precursor stream to flow into the chamber with the substrate therein under conditions effective to chemical vapor deposit a dielectric layer over the substrate. The boron source precursor can be combined with a nitrogen source and a carbon source.

Additional embodiments of the present invention include integrated circuit devices with a boron carbo-nitride layer. The boron carbo-nitride layer may comprise an amorphous boron carbo-nitride layer.

Further embodiments of the present invention include integrated circuit devices comprising a copper interconnect structure including an amorphous boron carbo-nitride barrier layer.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIGs. 1 shows a cross-sectional view of a copper interconnect.

FIG. 2 depicts an cross-sectional view of a copper interconnect.

FIG. 3 illustrates the XP spectra of B 1s, C 1s, and N 1s for films deposited on SiO<sub>2</sub> using DMAB with no reactive gas at (a) 360°C, (b) 420°C, and (c) 500°C.

FIG. 4 shows the XRD spectra of (a) 134 nm film deposited at 500°C using DMAB without reactive gas, (b) 67 nm film deposited at 360°C using DMAB with 10 sccm NH<sub>3</sub>, and (c) 7.1 nm SiO<sub>2</sub>/Si substrate.

FIG. 5 depicts the XP spectra of B 1s and N 1s for films deposited on SiO<sub>2</sub> at 360°C using DMAB with either (a) 0 or (b) 10 sccm NH<sub>3</sub>.

#### DETAILED DESCRIPTION OF THE EMBODIMENTS

The foregoing and other aspects of the present invention will now be described in more detail with respect to the embodiments described herein. It should be appreciated that the invention may be embodied in different forms and should not be construed as limited to the embodiments set forth herein. Rather, these embodiments are provided so that this disclosure will be thorough and complete, and will fully convey the scope of the invention to those skilled in the art. It will be understood that when an element such as a layer, a region or an integrated circuit device is referred to as being “on” another element, it can be directly on the other element or intervening elements may also be present. It will be understood that if part of an element, such as a surface of a conductive line, is referred to as “outer”, it is closer to the outside of the integrated circuit than other parts of the element. Furthermore, relative terms such as “beneath” may be used herein to describe a relationship of one layer or region to another layer or region relative to an integrated circuit device or a base layer as illustrated in the figures. It will be understood that these terms are intended to encompass different orientations of the device in addition to the orientation depicted in the figures. Finally, the term “directly” means that there are no intervening elements.

As newer low dielectric materials with lower thermal stability are being used, the maximum temperatures to which semiconductor devices can be subjected during manufacture decreases.  $BC_x$  and  $BC_xN_y$  films are two films that can have a low dielectric constant. Embodiments of the invention can include boron carbo-nitride layers and methods of forming boron carbo-nitride layers for use, for example, as a barrier and etch stop layer in copper interconnect structures used in integrated circuit manufacturing.

In some instances, there are problems attendant upon conventional copper interconnect methodology employing a diffusion barrier layer. However, due to copper diffusion through dielectric interlayer materials, such as silicon dioxide, copper interconnect structures must be encapsulated by a diffusion barrier layer. Currently, a combination of conductive and insulating barriers are employed. Typical conducting diffusion barrier metals include tantalum (Ta), tantalum nitride (TaN), titanium nitride (TiN), titanium-tungsten (TiW), tungsten (W), tungsten nitride (WN), Ti--TiN, titanium silicon nitride (TiSiN), tungsten silicon nitride (WSiN), tantalum silicon nitride (TaSiN). Typical insulating diffusion barriers materials include silicon nitride (SiN), silicon carbo-nitride (SiCN), silicon carbide (SiC) for encapsulating copper. The use of such barrier materials to encapsulate copper is not limited to the interface between copper and the dielectric interlayer, but can also include interfaces with other metals. However, some capping layers, such as silicon carbide, exhibit poor adhesion to the copper or copper alloy surface. Consequently, the capping layer is vulnerable to removal, as by peeling due to scratching or stresses resulting from subsequent deposition of layers. As a result, the copper or copper alloy is not entirely encapsulated and copper diffusion occurs thereby adversely affecting device performance and decreasing the electromigration resistance of the copper or copper alloy interconnect member. Boron carbo-nitride layers can alleviate some of these problems.

FIG. 1 illustrates a cross-sectional view showing a boron layer deposited via a thermal CVD method. In FIG. 1, a BCN insulating Cu diffusion barrier/etch stop 105 is deposited by CVD on a substrate 100. An interlayer dielectric material, trench low-k, (e.g., PECVD OSG) 110, may be formed on top of the barrier/etch stop 105. A trench or groove trench or groove pattern can be first etched into the interlayer dielectric material 110. A conductive Cu diffusion barrier 115, such as Ta or TaN

may be formed within the trench or groove pattern. Copper may then fill the trench or groove pattern. The top surface would then be subject to chemical-mechanical polishing (CMP). A BCN insulating Cu diffusion barrier/etch stop 125 can be formed covering the copper and interlayer dielectric material. This process can be repeated as shown. An interlayer dielectric material 130 is first formed. A BCN trench etch stop 135 (dual inlaid integration) may then be prepared on the interlayer dielectric material 130. A second interlayer dielectric material 140 may be formed on the BCN trench stop 135 and then a trench or groove trench or groove pattern 150 can be etched as noted above. Additionally, the etching may include a copper via 132. Again, a conductive Cu diffusion barrier 145 may be formed within the trench or groove pattern 150. The top surface would then be subject to chemical-mechanical polishing (CMP). Then a BCN insulating Cu diffusion barrier/etch stop 155 would be formed on the copper and interlayer dielectric material 140.

FIG. 2 also illustrates a cross-sectional view illustrates of a barrier layer surrounding a copper interconnect. Again, a BCN insulating Cu diffusion barrier/etch stop 105 is deposited by CVD on a substrate 100. An opening has been created in the layer 110 of dielectric, a BCN pore seal 111 comprising  $BC_xN_y$  has been deposited over the surfaces of the opening created in the surface of layer 110 of dielectric. A conductive Cu diffusion barrier 115 has been formed over the BCN pore seal 111. Copper interconnect 120 has been created overlying the BCN pore seal 111 and conductive Cu diffusion barrier 115, using conventional methods of metal deposition and polishing for the purpose of removing excess copper from the surface of layer 110 of dielectric. An etch stop layer 125 is then typically provided over the surface of layer 110 of dielectric, overlying the surface of copper interconnect 120, after which the process of creating a semiconductor device can proceed with the deposition of an additional layer 130. Additionally, the etch stop layer 125 can comprise  $BC_xN_y$ . This process can then be repeated to add a second layer as discussed with respect to FIG. 1.

Embodiments of the present invention can include forming a boron carbonitride layer on a substrate by chemical vapor deposition using a boron source precursor comprising  $NR_3:BX_3$ , wherein X is hydrogen and/or halide and wherein R is hydrogen, alkyl, allyl, alkenyl, alkynyl alkylaryl, arylalkyl, phenyl, alkene and/or alkyne.

Boron carbo-nitride films can be deposited by a thermal chemical vapor deposition (CVD) process using a boron source precursor that includes an amine with hydrogen and/or alkyl groups bonded to  $\text{BH}_3$ , such as a dimethylamineborane complex  $(\text{CH}_3)_2\text{NH}:\text{BH}_3$ . Ammonia and/or hydrocarbon (e.g., ethylene) reactive gases may be used as nitrogen and carbon sources, respectively. Precursors according to embodiments of the invention may have the composition  $\text{NR}_3:\text{BH}_3$ , where R can be any combination of H or alkyl, allyl, alkenyl, alkynyl alkylaryl, arylalkyl, phenyl, alkene and alkyne ligands. The combined ligands may be the same or different. Alkyl ligands may include, but are not limited to, methyl ( $\text{CH}_3$ ), ethyl ( $\text{CH}_3\text{CH}_2$ ), n-propyl ( $\text{CH}_3\text{CH}_2\text{CH}_2$ ), and isopropyl ( $(\text{CH}_3)_2\text{CH}$ ).

Boron carbo-nitride films formed according to some of the embodiments of the invention may be uniformly deposited on patterned structures, such as those commonly used in copper interconnect processing, at temperatures below  $450^\circ\text{C}$ , which can limit thermal exposure. In certain embodiments, boron carbo-nitride films may be formed between about  $360^\circ\text{C}$  and about  $500^\circ\text{C}$ . Films formed according to some embodiments of the invention can provide desirable electrical properties, such as a dielectric constant less than 4 and low leakage current. Films formed according to some embodiments of the invention can have their composition controlled by varying process parameters to enhance film properties such as adhesion, etch selectivity, and copper barrier effectiveness. Films according to some embodiments of the invention can have a composition  $\text{BC}_x\text{N}_y$ , where x can range from about 0 to about 2 and wherein y can range from about 0 to about 1.

Embodiments of the present invention can also include thermal chemical vapor deposition methods. According to some embodiments, a semiconductor substrate is provided in a chemical deposition chamber. A boron source precursor is vaporized by a vaporizer to form a flowing vaporized precursor stream, which is directed to flow into the chamber where it contacts the substrate under conditions effective to chemical vapor deposit a dielectric layer over the substrate. The boron source precursor can be  $\text{BX}_3$ , where X can be hydrogen and/or halide and may be the same or different. The boron source precursor can be combined with a nitrogen source and a carbon source, either in parallel flow with the precursor or in series flow or combination thereof. The nitrogen source can be any amine such as ammonia. The carbon source can be a hydrocarbon such as alkyl, allyl, alkenyl, alkynyl alkylaryl,

arylalkyl, phenyl, alkene and/or alkyne. Again, the thermal chemical vapor deposition occurs at between about 360°C and about 500°C. The layer deposited on the integrated circuit device can have a thickness of from about 20 Angstroms to about 1000 Angstroms.

Additional embodiments of the present invention can include at least one different precursor, thus creating different layers to be deposited onto the integrated circuit device. The boron carbo-nitride layer can be deposited onto the integrated circuit device or onto on a first metal layer. Additional embodiments of the present invention can further include vaporizing a third precursor and depositing the third precursor on one of the previously formed layers. Any number of precursors may be utilized in such embodiments.

Films formed according to embodiments of the present invention include films having dielectric constants less than 4, which are formed using thermal CVD at relatively low temperatures (e.g., between about 360°C and 500°C), which contrasts with conventional techniques that use plasma enhanced CVD. Additionally, the use of thermal CVD can provide for more uniformly deposited films and can reduce or avoid the substrate damage associated with plasma enhanced processes. Embodiments of the invention can reduce capacitance associated with interconnect insulating films by providing a lower dielectric constant material with reduced substrate damage.

Films formed according to embodiments of the invention can also be useful for uniformly sealing exposed pores inherent in porous dielectric films used in interconnect manufacturing. Sealing such pores may reduce or prevent diffusion of conductive precursors into the insulating film. Such use of a boron carbo-nitride film can offer a more conformal deposition within the pores and substrate patterns than conventional films deposited by plasma-enhanced processes, while also offering a low dielectric constant material that can limit increases to interconnect capacitance.

Films formed according to embodiments of the invention can also be useful for encapsulating the copper without the use of a conductive barrier.

Additional embodiments of the present invention can include integrated circuit devices with boron carbo-nitride layer. The boron carbo-nitride layer can be amorphous and can also be uniform. The amorphous boron carbo-nitride layer can be  $BC_xN_y$ , wherein x is in a range from about 0 to about 2, and wherein y is in a range from about 0 to about 1. The amorphous boron carbo-nitride layer can be deposited



as a film on a dielectric surface, such as  $\text{SiO}_2$ , using various BCN complexes at temperatures ranging from 360°C to 500°C and with varying  $\text{NH}_3$  flow at about 360°C. The BCN source can be formed by combining a boron source precursor with a nitrogen source and a carbon source. The nitrogen source can be any amine such as ammonia. The carbon source can be a hydrocarbon such as alkyl, allyl, alkenyl, alkynyl alkylaryl, arylalkyl, phenyl, alkene and/or alkyne. A higher nitrogen content can be incorporated into the film to create an amorphous film with smooth surface.

The present invention is explained in greater detail in the Examples that follow. These examples are intended as illustrative of the invention and are not to be taken as limiting thereof.

## EXAMPLES

### Example 1 – Creation of BCN Films

Films were deposited by thermal CVD in a film deposition facility consisting of a load lock, sample transfer system, CVD growth chamber, and analysis chamber equipped with X-ray photoelectron spectroscopy (XPS) as described elsewhere. Dimethylamine borane complex  $[\text{NH}(\text{CH}_3)_2\text{:BH}_3]$  (DMAB) from Strem Chemicals, Inc. (97% purity) was delivered to the reactor from a stainless steel bubbler containing approximately 50 g of the precursor and held at 27°C. DMAB is a white crystalline solid with a melting point of 36°C and vapor pressure of 1.5 mTorr at 60°C. DMAB vapor was carried to the reactor by ultra high purity (UHP, 99.999%) argon carrier gas delivered through a mass flow controller. UHP ammonia was also delivered through a mass flow controller. Reaction gas streams were mixed at the reactor doser approximately 4 cm above the substrate surface to minimize gas phase reactions while allowing uniform gas mixing at the substrate surface.

The films were deposited on 7.1 nm gate-quality thermal  $\text{SiO}_2$  substrates cleaved to 2.2 cm by 2.2 cm squares and mounted on a stainless steel sample holder. Substrates were cleaned prior to film deposition by sequentially using acetone, ethanol, and deionized water. The oxide substrates were then annealed under vacuum to minimize surface contamination. Samples were analyzed *in-situ* by XPS using a Physical Electronics 5500™ system at a 56.7 eV pass energy. Scanning electron microscopy (SEM) analysis used a LEO 1530™ electron microscope. X-ray diffraction (XRD) was performed with a Phillips automated vertical scanning general

purpose diffractometer. Surface roughness measurements were taken on a TM Microscopes AutoProbe CPR™ atomic force microscope (AFM) on 500 nm by 500 nm scanned areas in non-contact mode using a 1 Hz scan rate.

Metal-insulator-metal (MIM) test structures were created using a SSM 495CV™ Hg probe. The capacitance-voltage characteristics of the MIM device were measured using a probe tip area,  $A_{MIM}$ , of approximately  $3.5 \times 10^{-3} \text{ cm}^2$  (calibrated using a  $\text{SiO}_2$  standard) at a frequency of 100 kHz. The total average MIM device capacitance in accumulation mode ( $C_{acc}$ ) was determined from multiple scans on each sample. Samples were mounted on 6 in aluminum discs using silver paste to enable analysis of the small sample squares on a tool designed for larger samples. The accuracy of this mounting technique was verified using known  $\text{SiO}_2$  samples. The boron carbo-nitride film thickness ( $d_{BCN}$ ) was determined using a two parameter Cauchy film model on spectroscopic ellipsometry (SE) results collected at 70°C and 75°C on a J. A. Woollam VASE™ instrument (either an M2000-D™ or M-44™ model). Since the MIM device used represents two capacitors ( $C_{oxide}$  and  $C_{BCN}$ ) in series, the dielectric constant of the boron carbo-nitride ( $k_{BCN}$ ) film was calculated using Equation 1,

$$k_{BCN} = \frac{d_{BCN}}{\epsilon A_{MIM}} \left( \frac{1}{C_{acc}} - \frac{1}{C_{oxide}} \right)^{-1} \quad (1)$$

with  $\epsilon$  being the permittivity of free space. Boron carbo-nitride films deposited at thicknesses ranging from 20 to 130 nm were used to confirm the dielectric constant results, with no thickness dependence observed.

#### **Example 2 – The Effect of Temperature on Film Deposition and Properties**

Films were deposited by CVD on thermal  $\text{SiO}_2$  over a temperature range from 360°C to 500°C at 1 Torr to investigate the effect of substrate temperature on film deposition and properties. The argon carrier gas flow rate was held constant through the DMAB bubbler at 10 sccm. Table I shows the film composition measured *in-situ* using XPS for different deposition temperatures and the resulting dielectric constant. As the temperature increased, the boron concentration decreased while both the nitrogen and carbon increased. Oxygen was under 3 atom (at) % in the films, decreasing with temperature. Films deposited had a stoichiometric composition of  $\text{BC}_{0.26}\text{N}_{0.15}$  at 360°C,  $\text{BC}_{0.37}\text{N}_{0.17}$  at 420°C, and  $\text{BC}_{0.37}\text{N}_{0.25}$  at 500°C. The deposition temperature affected the film deposition rate. The deposition rate was 0.34 nm/min at

360°C, 1.5 nm/min at 420°C, and 4.0 nm/min at 500°C. Table I shows the dependence of the dielectric constant with deposition temperature, increasing from 4.62 at 360°C to 4.83 at 500°C. The index of refraction,  $n$ , measured at 633 nm by SE, also increased with temperature. The average  $n$  measured for films deposited at 360°C, 420°C, and 500°C was 2.069, 2.172, and 2.226, respectively.

**Table I**

T °C	%B	%C	%N	%O	$k$
360	69.4	10.5	17.8	2.3	4.62
420	64.5	10.8	23.8	0.9	4.65
500	61.6	15.5	22.5	0.4	4.83

FIG. 3 illustrates the high-resolution XP spectra of boron (B 1s), carbon (C 1s), and nitrogen (N 1s) for films deposited at different substrate temperatures. Line (a) illustrates a deposition at 360°C. Line (b) represents the boron carbo-nitride film being deposited at 420°C. Line C depicts a film deposited at 500°C. The B 1s peak shifts to higher binding energy (BE) as the temperature increases, with the shift corresponding to a change in chemical states from predominantly B-B bonding (188.3 eV) at 360 °C to higher levels of B-C (189.2 eV) and B-N (190.4 eV) bonding. The C 1s and N 1s XP spectra each show an increase in the bonding to boron as temperature increases along with a decrease in the bonding of carbon to nitrogen.

FIG. 4 shows the XRD result for a 134 nm film deposited at 500°C on SiO<sub>2</sub> (a), with the proportional intensities of crystalline *h*-BN as a reference shown at the bottom. FIG. 4 also illustrate (b) 67 nm film deposited at 360°C using DMAB with 10 sccm NH<sub>3</sub> and (c) 7.1 nm SiO<sub>2</sub>/Si substrate. The relatively featureless spectra indicate that the film is amorphous. The film surface roughness was measured using atomic force microscopy. The root-mean-square (RMS) surface roughness of the films increased with temperature, measuring 0.36 nm at 360°C, 0.44 nm at 420°C, and 0.53 nm at 500°C.

### **Example 3 – The Effect of Ammonia on Film Deposition and Properties**

Films were deposited by CVD on thermal SiO<sub>2</sub> at 360°C and 1 Torr to study the effect of NH<sub>3</sub> as a reaction gas on films deposited using DMAB. The argon carrier gas flow rate was again held constant at 10 sccm through the DMAB bubbler, with the ammonia flow rate varied from 0 to 10 sccm. Table II shows the resulting

film composition and the dielectric constant for the films deposited with varying ammonia flow rate. As the ammonia flow rate increased the nitrogen content increased in the film, displacing carbon and some boron in the film. The nitrogen to boron ratio increased from 0.15 with no ammonia flow to 0.58 with 10 sccm ammonia. At 10 sccm ammonia, both the carbon and oxygen in the film decreased to less than 3%. The approximate deposition rate increased to 0.60 nm/min at the lower ammonia flow rates (0.4 and 1.0 sccm) compared to 0.34 nm/min for no ammonia flow, and decreased to 0.49 nm/min at 10 sccm. Ammonia flow rates higher than 10 sccm lowered the deposition rate further. The decrease in deposition rate at higher  $\text{NH}_3$  flow rates illustrates that the surface reaction becomes mass transfer limited from the DMAB as the higher  $\text{NH}_3$  flow dilutes the DMAB concentration at the surface.

**Table II**

sccm $\text{NH}_3$	%B	%C	%N	%O	<i>k</i>
none	69.4	10.5	17.8	2.3	4.62
0.4	67.0	22.6	9.3	1.1	4.51
1	65.0	27.2	7.0	0.9	4.45
10	61.1	35.7	2.8	0.5	4.32

The introduction of nitrogen into the films using ammonia illustrated a reduction in *k*. Table II shows the dielectric constant decreasing from 4.62 with no ammonia flow to 4.32 with 10 sccm  $\text{NH}_3$ . The value of *n* (633 nm) decreased with the same trend as the dielectric constant from 2.069 with no ammonia to 1.893 with 10 sccm ammonia. Using an even higher ratio of  $\text{NH}_3$  to argon carrier flow (10:1) resulted in a film with a higher N:B ratio of 0.64 with *k* of 4.11 and *n* (633 nm) of 1.826.

FIG. 5 shows the B 1s and N 1s XP spectra for films deposited with no ammonia and 10 sccm ammonia. The addition of  $\text{NH}_3$  shifted the B 1s peak to a higher binding energy illustrating an increase in B-N bonding at 190.4 eV with a decrease in B-C and B-B bonding. The corresponding N 1s peak also displays a large increase in N-B bonding at 398.1 eV with the addition of  $\text{NH}_3$ . The XRD spectrum shown in FIG. 4 corresponding with line (b) for a 67 nm film deposited with 10 sccm  $\text{NH}_3$  shows no such evidence of crystalline order. High-resolution transmission electron microscopy and selected area electron diffraction analysis of a similar film deposited using 1 sccm  $\text{NH}_3$  further confirmed that the film has an amorphous

structure. RMS surface roughness measured by AFM ranged from 0.30 to 0.36 nm, with the higher roughness being for films deposited using 10 sccm  $\text{NH}_3$ .

The dielectric constant,  $k$ , of the films ranged from 4.11 to 4.83, with the lowest  $k$  found using high  $\text{NH}_3$  to precursor flow rates and the highest  $k$  resulted at 500 °C. The index of refraction changed consistently with the dielectric constant, ranging from 1.826 to 2.226. The films were found to be amorphous, with smooth RMS surface roughness ranging from 0.30 nm to 0.53 nm. Films deposited at higher temperature had higher nitrogen and carbon content due to the decomposition of the methylamine ligand on the precursor. The addition of ammonia led to N:B ratios as high as 0.64 within these process conditions, with nitrogen displacing carbon and some boron in the film composition, while reducing the dielectric constant.

It should be noted that many variations and modifications might be made to the embodiments described above without substantially departing from the principles of the present invention. All such variations and modifications are intended to be included herein within the scope of the present invention, as set forth in the following claims.